intoPIX Accelerates Automotive Innovation with TicoRAW & JPEG XS on Lattice Low Power FPGAs

From Sensors to Displays: The Most Efficient Automotive Compression Live at AutoSens Europe 2025

Mont-Saint-Guibert, Belgium, October 07, 2025 – intoPIX, the leading provider of low-latency image and video compression, announces the availability of its JPEG XS and TicoRAW technologies on Lattice Semiconductor's low power FPGA platforms –Avant™ and Nexus™– designed for automotive vision. The solution will be showcased at AutoSens Europe 2025.

This breakthrough enables Tier 1s and OEMs to integrate ultra-efficient image compression into ADAS and in-vehicle systems, delivering exceptional quality while minimizing bandwidth, power, and latency.

Optimized for Embedded Vision

Lattice FPGAs are known for low power, small footprint and flexibility, making them ideal for applications such as surround view, driver monitoring, sensor fusion, and autonomous driving.

With intoPIX compression IP cores now available:

TicoRAW compresses sensor data at the source without debayering, reducing dataflow while preserving critical features for AI-based perception and safety.

- **AI-Optimized**: reduces bandwidth and storage without compromising AI inference accuracy.
- **Multi-Sensor Compatible**: supports RAW Bayer and other CFA data for seamless deployment across diverse ecosystems.
- **Standardized & Future-Proof**: based on JPEG XS RAW, the only ISO standard for raw sensor data.
- **Sustainable**: lowers overhead and power for greener, more efficient vision systems.

JPEG XS ensures visually lossless quality with sub-millisecond latency. As an ISO standard, it enables reliable real-time video transmission while reducing implementation complexity.

- **Ultra-Low Latency**: delivers sub-millisecond delay, essential for safety-critical automotive pipelines.
- **ISO Standard**: guarantees interoperability across heterogeneous hardware and automotive Ethernet links.
- **Simple Integration**: minimizes implementation complexity, making it easy to deploy at scale.
- **Visually Lossless**: replaces uncompressed video without trade-offs in quality or reliability.
- **Proven Adoption**: widely used across industries as the ideal replacement for uncompressed video in real-time pipelines.

Together, TicoRAW and JPEG XS cover the full pipeline from raw capture to video transport. Unlike traditional codecs, intoPIX delivers guaranteed low-latency, efficiency, and minimal complexity for next-generation vision architectures.

Industry Voices

"By combining Lattice's efficient FPGAs with our TicoRAW and JPEG XS technologies, we provide OEMs with a unique solution to scale performance while overcoming design constraints," said Justine Hecq, Head of Automotive & Machine Vision at intoPIX.

Mark Hoopes, Sr. Director at Lattice, added: *"Integrating intoPIX on Lattice FPGAs enables up to 10X bandwidth reduction per camera, easing interconnects and paving the way for more scalable, efficient architectures."*

Live Demonstration at AutoSens

intoPIX will showcase TicoRAW and JPEG XS running on Lattice FPGAs at AutoSens Europe, 7–9 October 2025, Palau de Congressos, Barcelona (**Booth #327**). Visitors will see how the solution redefines automotive pipelines, enabling high-resolution capture, ultra-low-latency transmission, and power-efficient edge processing toward smarter, safer vehicles.

About intoPIX

intoPIX creates and licenses innovative image processing and compression solutions. We deliver unique IP-cores (ASIC/FPGA) and efficient software solutions (CPU/GPU) to manage more pixels, preserve quality with no-latency, save cost & power, and simplify storage and connectivity. Our solutions open the way to new AV workflows and new devices, reducing costs & replacing uncompressed video, and always preserving the lowest latency with the highest quality.
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